O I P EAppl. No. 10/607,869

JUN 1 0 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: Method of Forming Silicon-on-Insulator Comprising Integrated Circuitry

## **RESPONSE TO MARCH 10, 2005 OFFICE ACTION**

To:

Mail Stop Amendment Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

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## <u>AMENDMENTS</u>

## **Introductory Comments**

In reply to the Office Action of March 10, 2005, applicant amends and remarks as follows.